|  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- |
| **REF, ACTUAL CURRENT & VOLTAGE DPM PCB (PA79-557/B) TEST REPORT (017.01.008.0372 & 017.01.008.0373 )** | | | | | | | | | | | | | | | |
| **SR.NO.** | **Test point-->** | **TP1 wrt TP2** | **All seven segment displays, are working ok** | **TP4**  **(Actual**  **voltage calibration)**  **wrt TP2** | **TP5**  **(Actual Current Calibration)**  **wrt TP2** | **Check actual current on display @ different input voltages** | | | | **Check actual current on display @ 75 mV input.**  **Continuous for one minute.** | **TP6(Ref current calibration) wrt TP2**  **@Max ref 600A/ 400A Calibrated by P4** | **Short link LK3 for 600A**  **LK1 &**  **LK3 for 400A** |  | **Software Revision Sticker, Potting done & Silastic applied at trim pot.** | **REMARK (IF ANY REWORK IS DONE** | |
| Condition--> |  | **@Max 50V**  **Calibrated by P3**  **POT** | **@Max. 75mV input (calibrate by P1 POT)** | **@0.0 mV** | **@25**  **mV** | **@50 mV** | **@75 mV** | **Software rev** |
|  |  |  |
|  | +5Vdc  (+/-0.2V) | OK/NOT OK | 1.50 to 1.7 V (±0.2V) | 3 to 3.2 V (±0.2V**)** | 0.0-0.15 v | 1.0-1.20 v | 2.0 -2.2 v | 3.0-3.2 v | OK/  NOT OK | 3 to 3.2 V (±0.2V**)** | OK/  NOT OK | Mention rev | OK/  NOT OK |
| Message on Display for  017.01.008.0372 |  |  |  | **600 on display window >** | 15 or ref curr | 200  (±3) | 400  (±3) | 600  (±3) | Constant  600((±1)) | 600 on display |  |  |  |
| Message on Display for  017.01.008.0373 |  |  |  | **400 on display window >** | 15 or ref curr | 133  (±3) | 268  (±3) | 400  (±3) | Constant  400((±1)) | 400 on display |  |  |  |  | |
| PCB SR.NO. | Measured / Actual voltage | | | | | | | |  | |  |  |  |  |
| 1 |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  | |
| 2 |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  | |
| 3 |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  | |
| 4 |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  | |
| 5 |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  | |
| 6 |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  | |
| 7 |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  | |
| 8 |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  | |
| 9 |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  | |
| 10 |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  | |